UNIBAP

Unibap's iX5-200 Universal Payload Interface for Extreme Edge Computing in Space

iX5-200

The iX5-200 builds on the flight proven iX5-100 series but adds a bridge from the FPGA paradigm into Open Platform Computing. In addition to the CPU and GPU of a standard iX5, the iX5-200 includes an advanced Xilinx FPGA that provides high-speed interfacing and payload control for extreme Edge Computing and Autonomous Operation.



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NASAM

Unibap's iX10-200 Universal Payload Interface for High-Performance Space Missions



Model Name

PROCESSING & MEMORY	
Intelligent Processing Core	Unibap Qseven e2160 compute module (AMD Steppe Eagle CPU, AMD Radeon GPU)
RAM	2 GB DDR3 ECC (CPU/GPU) 1GByte DDR4 Xilinx (FPGA)
Storage	2 x 120 GB SATA SSD
FPGA	Xilinx KU060 Microsemi SmartFusion 2
I/O INTERFACE	
CAN v2.0b	ו × (Isolated)
Ethernet, GigaLAN	1 × GbE (1000BASE-T)
GPIO	21 × (single ended)
12C	4 ×
LVDS, general purpose	42 × (signal integrity routed for camera link)
Serial Communication	5 × RS232/422 (Isolated) 9 × RS422 (full duplex) 2 × RS485
SpaceWire	2 ×
SPI	2 × (master + slave)
Termistor inputs	20 × AD590
USB	2 × USB 2.0 1 × USB 3.0
Voltage outputs	4 × 5 V (regulated with LCL and U,I monitoring) 1 × 12 V (with LCL and U,I monitoring)
MECHANICAL	
Dimensions	155 (W) × 163 (H) × 54 (D) [mm] Request ICD for details
Mass	1300 grams
ENVIRONMENTAL & ELECTRICAL	
Power Consumption	20-30 W (Depending on processing and storage selection and use)
Vibration	Qualified for SpaceX Falcon9 launch, details upon request
Certification	IPC 610-E Class III (RoHS)
SOFTWARE SUPPORT	
Operating System	Unibap OS

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